

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

EXPRESS MAIL NO. EV 327292141 US

Appl No. : Unknown
Applicant : Raymond B. Patterson III; Randy T. Heilman
Filed : Herewith - November 24, 2003
Title : METHOD OF OVERTONE SELECTION AND LEVEL CONTROL IN
AN INTEGRATED CIRCUIT CMOS NEGATIVE RESISTANCE
OSCILLATOR TO ACHIEVE LOW JITTER

TC/A.U. : 2817
Examiner : Not Assigned

Docket No. : 51592/DMC/V165
Customer No. : 23363

PRELIMINARY AMENDMENT

Commissioner for Patents	Post Office Box 7068
P.O. Box 1450	Pasadena, CA 91109-7068
Alexandria, VA 22313-1450	November 24, 2003

Commissioner:

Prior to examination, please amend the above-identified application as follows:

Amendments to the Specification begin on page 2 of this paper.

Remarks/Arguments begin on page 3 of this paper.